



EXPRESS MAIL NO. EV560405531US
Sheet 1 of 1

FORM PTO-1449 (REV.7-80) INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. 130209.511	APPLICATION NO. 10/658,124
	APPLICANT Venkatapathi R. Nallapa		
	FILING DATE September 9, 2003	GROUP ART UNIT 2818 2186	

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
SE	AA	5,740,396	04/14/98	Mason	395 ⁷¹¹	430 ¹⁰³	—
	AB						
	AC						
	AD						
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	AH						
	AI						
	AJ						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION	
					YES	NO
SE	AK	4-105300	04/07/92	JP (+ Abstract in English)		X
SE	AL	8-17192	01/19/96	JP (+ Abstract in English)		X
SE	AM	0 991 081 A1	04/05/00	EP	N/A	
	AN					
	AO					

OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	AP	
	AQ	
	AR	

EXAMINER <i>SE</i>	DATE CONSIDERED 9/17/2005
* EXAMINER: Initial if reference considered, whether or not criteria is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).	

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SE	AM	"EEPROM Replacement with Flash Memory – Intel Application Note AP-685," Intel Corporation, Denver, CO, December 1998, pp 1-20.					
SE	AN	Hazen, P., "Using Intel's Boot Block Flash Memory Parameter Blocks to Replace Eeprom - Intel Application Note AP-604," Intel Corporation, Denver, CO, November 1995, pp 1-6.					
SE	AO	"Am29F200B Data Sheet," Advanced Micro Devices (AMD), Publication # 21526, Rev: C Amendment/+2, Sunnyvale, CA, November 1999, pp 1-39.					
SE	AP	STMicroelectronics, "Flash+, Exit the Emulation Compromise," ST Challenge, 4th Edition, 1997, retrieved from http://us.st.com/stonline/press/magazine/challeng/4thedit/chal5.htm , pp 1-3.					
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